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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/765,197	01/28/2004		Mutsumi Kimura	118211 2520	
25944	7590	10/17/2005		EXAMINER	
OLIFF & B P.O. BOX 19		E, PLC	SOWARD, IDA M		
ALEXANDI		22320	ART UNIT	PAPER NUMBER	
				2822	

DATE MAILED: 10/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)				
Office Astion Commons	10/765,197	KIMURA, MUTSUMI				
Office Action Summary	Examiner	Art Unit				
	Ida M. Soward	2822				
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	correspondence address –				
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING [- Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by stature to reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION 136(a). In no event, however, may a reply be tin I will apply and will expire SIX (6) MONTHS from te, cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 03 /	August 2005.					
•	is action is non-final.					
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ⊠ Claim(s) <u>1-7</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdra 5) ⊠ Claim(s) <u>6 and 7</u> is/are allowed. 6) ⊠ Claim(s) <u>1-5</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/	awn from consideration.					
Application Papers						
9) The specification is objected to by the Examin						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to the	* * *					
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E						
Priority under 35 U.S.C. § 119		•				
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of: 1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	nts have been received. Its have been received in Applicationity documents have been received au (PCT Rule 17.2(a)).	on No ed in this National Stage				
Attachment(s)	_					
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) 	4) Interview Summary Paper No(s)/Mail D					
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date		Patent Application (PTO-152)				

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DETAILED ACTION

This Office Action is in response to the Applicants amendment filed August 3, 2005.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-5 are rejected under 35 U.S.C. 102(e) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Satake (US 6,798,132 B2).

In regard to the limitations a first substrate; a second substrate; a third substrate; active elements formed on the first substrate; wiring lines formed on the second substrate, electro-optical elements formed on the third substrate; and an element chip having at least one active element peeled off from the first substrate and transferred onto at least one of the second substrate and the third substrate, "[E]ven though product-by-process claims are limited by and defined by the process, determination of patentability is based on the product itself. The patentability of a product does not depend on its method of production. If the product in the product-by-process claim is the same as or obvious from a product of the prior art, the claim is unpatentable even though the prior product was made by a different process." In re Thorpe, 777 F.2d 695,

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698, 227 USPQ 964, 966 (Fed. Cir. 1985) See § MPEP 2113. note that a "product by process" claim is directed to the product per se, no matter how actually made, In re Hirao, 190 USPQ 15 at 17 (footnote 3). See also In re Brown, 173 USPQ 685; In re Luck, 177 USPQ 523; In re Wertheim, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); In re Fitzgerald, 205 USPQ 594, 596 (CCPA); In re Marosi et al., 218 USPQ 289 (CAFC); and most recently, In re Thorpe et al., 227 USPQ 964 (CAFC, 1985) all of which make it clear that it is the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that, as here, an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims or not. Note that Applicant has burden of proof in such cases as the above case law makes clear.

In regard to claim 1, Satake teaches the final product of an active matrix display device, comprising: a second substrate adhering to the third substrate, the active elements 121 being electrically connected to the wiring lines 124 & 125 on one surface of the element chip facing the second substrate 101, and the active elements 121 being electrically connected to the electro-optical elements 120 on another surface of the element chip facing the third substrate 100 (Figure 1, columns 9-10, lines 13-67 and 1-4, respectively).

In regard to claim 2, Satake teaches the electrical connections being performed by conductive material (Figure 1, column 9, lines 56-60).

In regard to claim 3, Satake teaches the conductive material being disposed in a position corresponding to the periphery of the element chip between the element chip

and the second 101 or third 100 substrate, and the active elements 121 being electrically connected to the electro-optical elements 120 (Figure 1, columns 9-10, lines 13-67 and 1-4, respectively).

In regard to claim 4, Satake teaches an opening being formed in the element chip, the conductive material being disposed in a position corresponding to the opening between the element chip and at least one of the second substrate 101 and third substrate 100, and the active elements 121 being electrically connected to the electro-optical elements 120 (Figure 1, columns 9-10, lines 13-67 and 1-4, respectively).

In regard to claim 5, Satake teaches the active elements 121 being thin film transistors (Figure 1, column 10, lines 5-11).

Allowable Subject Matter

Claims 6-7 are allowed.

Response to Arguments

Applicant's arguments filed August 3, 2005 have been fully considered but they are not persuasive. In regard to the remarks concerning claims 1-5, Satake teaches the final device structure.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

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The following patents are cited to further show the state of the art with respect to active matrix display devices:

Kato et al. (5,789,763)

Yudasaka et al. (US 6,593,591 B2).

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ida M. Soward whose telephone number is 571-272-1845. The examiner can normally be reached on Monday - Thursday 6:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

IMS

October 13, 2005